

ABSTRACT OF THE DISCLOSURE

A liquid crystal display includes a liquid crystal panel including a plurality of pixel rows, a plurality of data lines for transmitting data voltages to the pixel rows, a plurality of gate lines for transmitting gate signals to the pixel rows. The pixel rows includes a plurality of pairs of
5 first and second pixel rows adjacent to each other. The first and the second pixel rows sequentially arranged in a data voltage moving direction and supplied with the data voltages having different polarities. The gate signals include first and second gate signals respectively applied to the first and the second pixel rows, and pulse widths of the second gate signals are increased by first modulation amounts.